Application No.: 10/591,644 Docket No.: 1163-0582PUS1

Amendment Dated December 30, 2008

Reply to Office Action of September 30, 2008

AMENDMENTS TO THE CLAIMS

1 - 8. (Canceled)

9. (Currently Amended) The A high efficiency amplifier including input signal dividing

means for splitting an input signal, comprising: according to claim 8, further comprising:

a first amplifier for amplifying a first input signal fed from said input signal dividing

means;

a second amplifier for amplifying, when power of a second input signal fed from said

input signal dividing means is greater than specified power, the second input signal;

a combining circuit for combining an output signal of said first amplifier and an output

signal of said second amplifier, said high efficiency amplifier comprising before said first

amplifier;

a first waveform shaping circuit for limiting, when amplitude of the first input signal fed

from said input signal dividing means is greater than a specified level, the amplitude of the first

input signal to less than the specified level, and for supplying to said first amplifier;

a second waveform shaping circuit for suppressing the output of a signal when amplitude

of the second input signal fed from said input signal dividing means is less than a specified level

before said second amplifier;

a first distortion compensation circuit for compensating for nonlinear distortion of said

first amplifier before said first amplifier;

a second distortion compensation circuit for compensating for nonlinear distortion of said

second amplifier before said second amplifier;

a first adaptive control circuit for varying parameters of said first distortion compensation

circuit in response to the output signal of said first amplifier; and

a second adaptive control circuit for varying parameters of said second distortion

compensation circuit in response to the output signal of said second amplifier.

10. (Canceled)

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11. (Original) The high efficiency amplifier according to claim 10claim 9, wherein said first waveform shaping circuit has a characteristic of gradually limiting the amplitude of the input signal as the amplitude of the input signal approaches the specified level.

12 - 13. (Canceled)